

ABSTRACT

A printer is described. The printer includes a CPU, a DMA controller and an internal memory. The CPU and the DMA controller are each connected to a first memory bus. The internal memory is connected to a second memory bus. The printer further included an address translation unit
5 connected to both the first and the second memory bus. The address translation operates to receive virtual addresses from both the CPU and the DMA controller over the first memory bus and translates these virtual addresses into physical addresses. The address translation unit transmits the physical addresses to the memory over the second memory bus.

[illegible]